

Freeform Search

Database:	<input type="checkbox"/> US Pre-Grant Publication Full-Text Database <input checked="" type="checkbox"/> US Patents Full-Text Database <input type="checkbox"/> US OCR Full-Text Database <input type="checkbox"/> EPO Abstracts Database <input type="checkbox"/> JPO Abstracts Database <input type="checkbox"/> Derwent World Patents Index <input type="checkbox"/> IBM Technical Disclosure Bulletins
Term:	<input type="text" value="L36 and @py<=2003"/>
Display:	<input type="text" value="20"/> Documents in <u>Display Format:</u> <input type="text" value="TI"/> Starting with Number <input type="text" value="1"/>
Generate: <input type="radio"/> Hit List <input checked="" type="radio"/> Hit Count <input type="radio"/> Side by Side <input type="radio"/> Image	
<input type="button" value="Search"/> <input type="button" value="Clear"/> <input type="button" value="Interrupt"/>	

Search History

DATE: Thursday, May 25, 2006 [Printable Copy](#) [Create Case](#)

<u>Set</u>	<u>Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set</u>
				<u>Name</u> result set
		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ		
<u>L37</u>		L36 and @py<=2003	52	<u>L37</u>
<u>L36</u>		row adj address adj input\$1 same column adj address adj input\$1 same latch\$1	71	<u>L36</u>
<u>L35</u>		row adj address adj input\$1 same column adj address adj input\$1 same lattach\$1	0	<u>L35</u>
<u>L34</u>		L33 and addressable adj latch	1	<u>L34</u>
<u>L33</u>		row adj address adj input\$1 same column adj address adj input\$1	486	<u>L33</u>
<u>L32</u>		l27 and (row or column) adj address adj input\$1	1	<u>L32</u>
<u>L31</u>		L27 same pixel\$1 same display\$1	2	<u>L31</u>
<u>L30</u>		L27 and row adj address adj input	1	<u>L30</u>
<u>L29</u>		L2 and row adj address adj input	0	<u>L29</u>
<u>L28</u>		addressable adj latch\$1 same (row or column) address adj input\$1	1	<u>L28</u>
<u>L27</u>		addressable adj latch\$1	318	<u>L27</u>
<u>L26</u>		L25 and @py<=2003	1076	<u>L26</u>
<u>L25</u>		pixel same coupl\$6 same (thin-film-transistor or TFT or thin adj film adj	2177	<u>L25</u>

	transistor)		
<u>L24</u>	l21 and thin-film-transistor same (display adj device\$1 or pixel\$1)	6	<u>L24</u>
<u>L23</u>	l21 and thin-film-transistor adj (display adj device\$1 or pixel\$1)	0	<u>L23</u>
<u>L22</u>	l21 and thin-film-transistor adj (display adj device\$1 of pixel\$1)	0	<u>L22</u>
<u>L21</u>	L20 and @py<=2003	583	<u>L21</u>
<u>L20</u>	(display adj element or pixel) same coupled same (switch or transistor) same capacitor	1234	<u>L20</u>
<u>L19</u>	(display adj element or pixel) same coupled same (switch or transistor) same capacator	0	<u>L19</u>
<u>L18</u>	(display adj element or pixel) same coupled same (switch or transistor)	4427	<u>L18</u>
<u>L17</u>	(latch or logic adj gate)same (switch or transistor) adj gate	2015	<u>L17</u>
<u>L16</u>	(latch or logic adj gate)same transistor adj gate	1784	<u>L16</u>
<u>L15</u>	address\$6 adj latch\$1 same input same (switch or transistor\$1) same pixel\$1	9	<u>L15</u>
<u>L14</u>	address\$6 adj latch\$1 same input same (switch or transistor\$1)	578	<u>L14</u>
<u>L13</u>	L12 and @py<=2003	52	<u>L13</u>
<u>L12</u>	(display or pixel\$1) same addressable adj latch	54	<u>L12</u>
<u>L11</u>	addressable adj latch	445	<u>L11</u>
<u>L10</u>	column adj address comparator same (display adj element\$1 or pixel\$1)	5	<u>L10</u>
<u>L9</u>	column adj address comparator	71	<u>L9</u>
<u>L8</u>	L7 column adj address comparator	0	<u>L8</u>
<u>L7</u>	row adj address adj comparator	118	<u>L7</u>
<u>L6</u>	L5 and pixel near 2 latch	6	<u>L6</u>
<u>L5</u>	pixel\$1 same latch	6043	<u>L5</u>
<u>L4</u>	pixel\$1 and addressble adj latch	0	<u>L4</u>
<u>L3</u>	display\$ and addressble adj latch	0	<u>L3</u>
<u>L2</u>	display adj element\$1 and addressble adj latch	0	<u>L2</u>
<u>L1</u>	display adj element\$1 same addressble adj latch	0	<u>L1</u>

END OF SEARCH HISTORY

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side by side		result set	
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<u>L15</u>	address\$6 adj latch\$1 same input same (switch or transistor\$1) same	9	<u>L15</u>

<u>pixel\$1</u>		
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<u>L2</u>	display adj element\$1 and addressble adj latch	0 <u>L2</u>
<u>L1</u>	display adj element\$1 same addressble adj latch	0 <u>L1</u>

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